Application No.: 09/922,045   Docket No.: JCLA6649

In The Claims:

Claim 1. (Currently Amended) A sequencing method for accessing shared system resources capable of determining the priority of transactions initiated by a plurality of master controllers, comprising the steps of:

providing each transaction with a transaction identification value to determine an order of execution of each transaction;

providing each transaction with a master identification value to label the initiating master controller of each transaction; and

gathering transactions having an identical master identification value and accessing the shared system resource in sequence according to the transaction identification value, comprising the steps of:

picking up the transaction having the smallest transaction identification value and executing the transaction to access the shared system resource; and

executing the write transactions to access the shared system resource before the read transactions if two or more transactions have the same smallest transaction identification value.

Claim 2. (Original) The method of claim 1, wherein the transactions include write transactions and read transactions.

Claim 3. (Previously Presented) The method of claim 2, wherein the step of providing each transaction with the transaction identification value includes the sub-steps of:

providing the first transaction with the transaction identification value of 0;

adding 1 to the transaction identification value when the previous transaction of the read transaction is another read transaction;

adding 0 to the transaction identification value when the previous transaction of the read transaction is a write transaction;

adding 1 to the transaction identification value when the previous transaction of the write transaction is a read transaction;

adding 0 to the transaction identification value when the previous transaction of the write transaction is another write transaction.

Claim 4. (Cancelled)

Claim 5. (Original) The method of claim 1, wherein the sequencing method also incorporates a flush and a fence signal provided by an accelerated graphic port (AGP) bus to ensure proper transaction execution sequence.

Claims 6-11 (Cancelled)